

Abstracts

Designing reliable high-power limiter circuits with GaAs PIN diodes

D.G. Smith, D.D. Heston, J. Heston, B. Heimer and K. Decker. "Designing reliable high-power limiter circuits with GaAs PIN diodes." 2002 MTT-S International Microwave Symposium Digest 02.2 (2002 Vol. II [MWSYM]): 1245-1247 vol.2.

This paper presents the latest GaAs PIN diode rf reliability data. An acceleration factor predicting lifetime as a function of diode perimeter and rf power level is proposed. Power handling measurements of an improved PIN diode layout that provides superior performance for both power handling and small signal loss/bandwidth is presented. Finally the design principles for a high power limiter will be reviewed.

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